

160

140

120

100

80

60

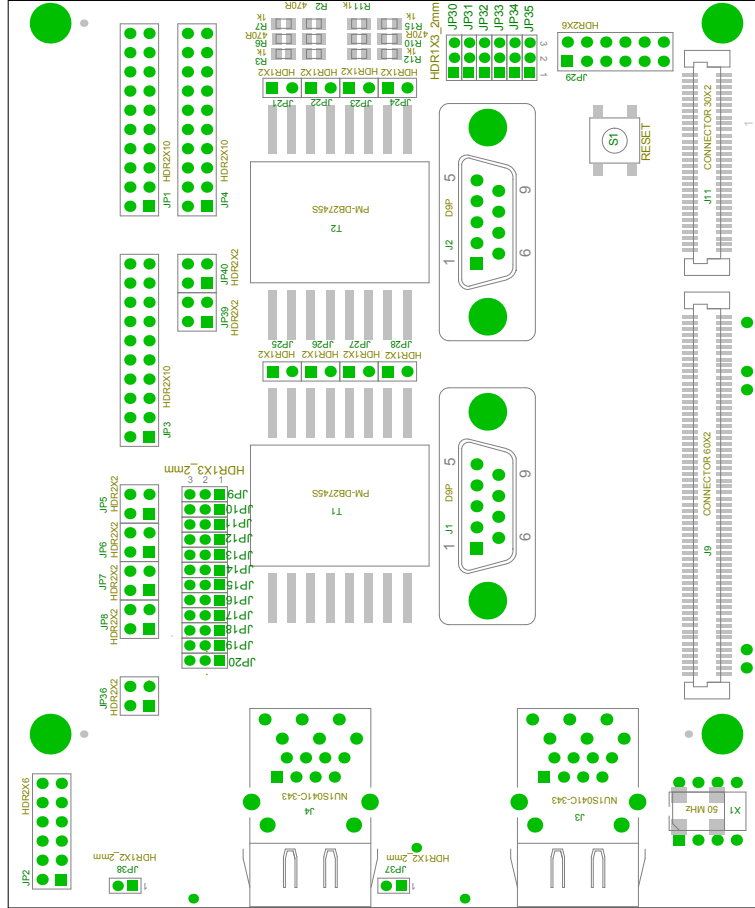
40

20

0



PENDER ELECTRONIC DESIGN
P/N PED-00114-PCB REV 0.1
24/02/2009



PENDER ELECTRONIC DESIGN
Staffelstrasse 8, Zurich, Switzerland www.pender.ch

TITLE: LEON Compact PCI Evaluation Board Custom Mezzanine Board
PROJECT GR-RTAX-MEZZ2 REV.0.1 DATE: 24/02/2009

FILE NAME: PED-00114-PCB_rev0-1.PCB



X1

0

20

40

60

80

100

120

140

160

180

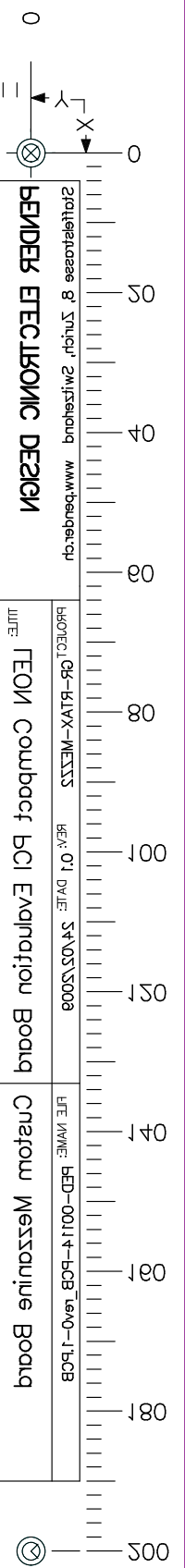
200



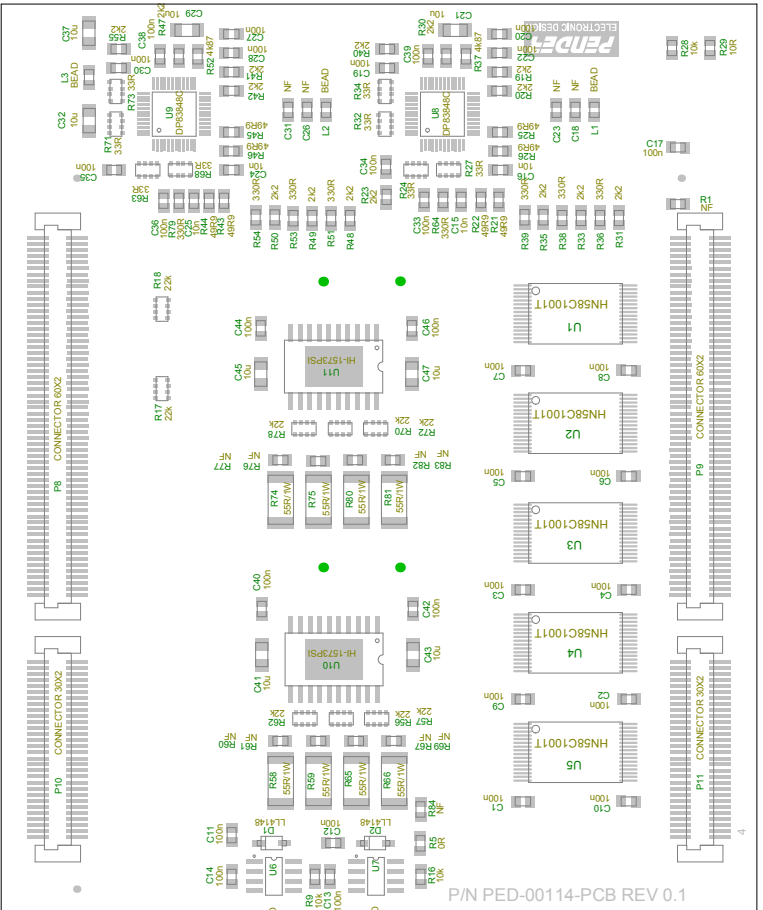
LAYER GERBER

Top layer .GTL

Mechanical 1 .GM1



2104161400008 g_12urcu_2w11zshqnd www.pcboard.ch
BENDER ELECTRONIC DESIGN
 PROJECT: GR-R1AX-MEZZS REF: 01 DATE: 24.03.2008
 TITLE: LEON Compact PCI Evaluation Board Custom Mezzanine Board
 FILE NAME: PED-00114-PCB_rev0-1.PCB



24.03.2008
 P/N PED-00114-PCB REV 0.1
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Mechanical 1	IPC
Bottom Layer	GER
LAYER	GERBER